

III. CLAIM AMENDMENTS

1. (Currently Amended) A semiconductor substrate cassette reducer for reducing a substrate holder capable of holding a substrate of a predetermined size, the cassette reducer comprising:

a first substantially U-shaped plate;

a second substantially U-shaped plate;

a plurality of wafer supports joining the first substantially U-shaped plate to the second substantially U-shaped plate; and

more than two retention springs attached to the first substantially U-shaped plate for mounting the cassette reducer to the substrate holder, wherein when mounted to the holder, the cassette reducer effects a reduction in the substrate holder enabling the holder to hold another substrate smaller than the predetermined size.

2. (Original) The semiconductor cassette reducer of claim 1, wherein the first substantially U-shaped plate has a pair of interior arm cutouts.

3. (Original) The semiconductor cassette reducer of

claim 1, wherein the first substantially U-shaped plate has a base cutout.

4. (Previously Presented) The semiconductor cassette reducer of claim 1, wherein a base to tip distance of the first substantially U-shaped plate is less than an interior depth of a front opening unified pod to which the semiconductor cassette reducer is adapted to be mated.

5. (Original) The semiconductor cassette reducer of claim 1, wherein the plurality of wafer supports includes a pair of side panels connected to a pair of arms of the first substantially U-shaped plates.

6. (Original) The semiconductor cassette reducer of claim 5, wherein the pair of side panels have a plurality of lips.

7. (Previously Presented) The semiconductor cassette reducer of claim 1, wherein the first substantially U-shaped plate has an exterior partial S-shaped cutout.

8. (Original) The semiconductor cassette reducer of claim 1, wherein the plurality of wafer supports includes a pair of columns.

9. (Previously Presented) The semiconductor cassette

reducer of claim 8, wherein the pair of columns have at least two positions.

10. (Currently Amended) A semiconductor cassette reducer, comprising:

a first substantially U-shaped plate having a first pair of arms each having a first arm cutout;

a second substantially U-shaped plate having a second pair of arms each having a second arm cutout;

a plurality of wafer supports longitudinally connecting the first substantially U-shaped plate to the second substantially U-shaped plate; and

at least one resiliently flexible retention member mounted on at least one of the first substantially U-shaped plate or the second substantially U-shaped plate, and projecting outward laterally from a peripheral edge of the at least one of the first substantially U-shaped plate or the second substantially U-shaped plate.

11. (Currently Amended) The [[A]] semiconductor cassette reducer of claim 10, [[comprising:]]

~~a first substantially U-shaped plate having a first pair of arms each having a first arm cutout;~~

~~a second substantially U-shaped plate having a second pair of arms each having a second arm cutout;~~

~~a plurality of wafer supports connecting the first substantially U-shaped plate to the second substantially U-shaped plate; and~~

~~and further including a plurality of retention springs attached to the first substantially U-shaped plate.~~

12. (Original) The semiconductor cassette reducer of claim 11, wherein one of the plurality of retention springs is designed to mate with a lip of a front opening unified pod.

13. (Original) The semiconductor cassette reducer of claim 10, wherein the plurality of wafer supports include a wafer support panel attached to one of the first pair of arms.

14. (Currently Amended) A semiconductor cassette reducer for a substrate holder, the cassette reducer comprising:

a first substantially U-shaped plate;

a second substantially U-shaped plate;

a first wafer support panel attached to a first arm of the first substantially U-shaped plate and to a first arm of the second substantially U-shaped plate; and

a second wafer support panel attached to a second arm of the first substantially U-shaped plate and to a second arm of the second substantially U-shaped plate; wherein

the first substantially U-shaped plate has a [[spring loaded]] retention [[member]] spring projecting outward from an outer edge of the first substantially U-shaped plate for engaging a surface of the substrate holder when the semiconductor cassette reducer is mounted to the substrate holder.

15. (Original) The semiconductor cassette reducer of claim 14, further including a pair of column wafer supports attached to a base of the first substantially U-shaped plate and to a base of the second substantially U-shaped plate.

16. (Previously Presented) A semiconductor cassette reducer comprising:

a first substantially U-shaped plate;

a second substantially U-shaped plate;

a first wafer support panel attached to a first arm of the first substantially U-shaped plate and to a first arm of the second substantially U-shaped plate; and

a second wafer support panel attached to a second arm of the first substantially U-shaped plate and to a second arm of the second substantially U-shaped plate;

wherein the first substantially U-shaped plate has a plurality of flexible disks.

17. (Original) The semiconductor cassette reducer of claim 14, wherein the first substantially U-shaped plate has a pair of arms each having an interior cutout,

18. (Previously Presented) The semiconductor cassette reducer of claim 14, wherein a base to tip distance of the first substantially U-shaped plate is less than a diameter of wafer designed for a front opening unified pod to which the semiconductor cassette reducer is adapted to be mated.